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Introduction to VLIW

Superscalar vs VLIW -

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Architecture - VLIW

Compiler

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Introduction to VLIW  
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Examples - Georgia

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Lec 11: Advanced

Pipelining /u0026

Superscalar

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SIMD vs VLIW RISC-V

Linux Dev Board;

ESP32 WT32 ETH01,

Raspberry Pi

DDJ-400, and More!

What is



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SUPERSCALAR

PROCESSOR? What  
does SUPERSCALAR  
PROCESSOR mean?

SUPERSCALAR

PROCESSOR meaning

High Performance  
Computer

Architecture - Supersc  
alar, Superpipelined  
and VLIW processors.

Superscalar Processor  
Processor Pipeline  
Stalls - Georgia Tech -

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HPCA: Part 1

Explaining CPU

Architecture:

Pipelining, Pipeline

Stages, Superscalar

CPUs and Order - Ep.

2 1 4 6 Loop

Unrolling

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Instruction Level

Parallelism (ILP) -

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in Hindi 2 2 3 VLIW

Challenges Register

File Complexity

/u0026 Control

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Processor by Er.

Dipesh Vaya VLIW

Architecture A

Russian CPU:

The 8-core

-8 !

(MCST Elbrus)

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Lecture 15. GPUs,

VLIW, Execution

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contemporary VLIW  
CPUs include the  
TriMedia media  
processors by NXP  
(formerly Philips  
Semiconductors), the

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Super Harvard

Architecture Single-

Chip Computer

(SHARC) DSP by

Analog Devices, the

C6000 digital signal

processor (DSP)

family by Texas

Instruments, the

ST200 family by

STMicroelectronics

based on the Lx

architecture

(designed in Josh

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Fisher's HP lab by  
Paolo Faraboschi),  
and the MPPA

Manycore family by  
Kalray. These

contemporary VLIW  
CPUs are mainly  
successful as

embedded ...

Very long instruction  
word - Wikipedia

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VLIW to TTA (Wiley Series in Microwave and Optical) by Corporaal (ISBN: 9780471971573) from Amazon's Book Store. Everyday low prices and free delivery on eligible orders.

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Microprocessor Architectures is cutting-edge text which will prove invaluable to both industrial hardware and software engineers involved in embedded system design and to postgraduate electrical engineering and computer science

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Abstract. From the  
Publisher: The market  
for single chip  
microprocessors is  
huge and their  
performance  
continues to increase,  
driven by the on-  
going demand for  
more powerful  
applications,  
particularly in the

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in computer  
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exciting concept:  
Transport Triggered  
Architecture (TTAs).  
Unlike most...

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Computer  
Architecture

ABSTRACT VLIW  
architectures are  
distinct from  
traditional RISC and

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CISC architectures implemented in current mass-market microprocessors. It is important to distinguish instruction-set architecture—the processor programming model—from implementation—the physical chip and its characteristics. VLIW

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microprocessors and  
superscalar  
architectures  
implementations of  
traditional  
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An Introduction To  
In Microwave  
Very-Long Instruction  
And Optical  
Word (VLIW ...

VLIW Architecture.

Very-Long Instruction  
Word (VLIW)

architectures are a  
suitable alternative  
for exploiting

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instruction-level parallelism (ILP) in programs, that is, for executing more than one basic (primitive) instruction at a time. These processors contain multiple functional units, fetch from the instruction cache a Very-Long Instruction Word containing several primitive instructions,



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and dispatch the  
entire VLIW for  
parallel execution.

VLIW Architecture -  
IBM

Introduction o Very  
long instruction word  
or VLIW refers to a  
processor

architecture designed  
to take advantage of  
instruction level  
parallelism o

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Instruction of a VLIW processor consists of multiple independent operations grouped together. o There are Multiple Independent Functional Units in VLIW processor architecture. o Each operation in the instruction is aligned to a functional unit. o All functional units share the use of a

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common large  
register file. o This  
type of processor ...

VLIW Processors -  
SlideShare

- change in the instruction set architecture, i.e., 1 program counter points to 1 bundle (not 1 operation) • want operations in a bundle to issue in

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parallel • fixed  
format so could  
decode operations in  
parallel • enough  
FUs for types of  
operations that can  
issue in parallel •  
pipelined FUs

Autumn 2006 CSE  
P548 - VLIW 2 VLIW  
Processors

VLIW Processors - co  
urses.cs.washington.e

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In computer architecture, a transport triggered architecture (TTA) is a kind of processor design in which programs directly control the internal transport buses of a processor.

Computation happens as a side effect of data transports:

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writing data into a triggering port of a functional unit triggers the functional unit to start a computation. This is similar to what happens in a systolic array.

Transport triggered architecture -  
Wikipedia

Advantages of  
*Page 30/44*

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Superscalar

Architecture : In a  
Superscalar

Processor, the

detrimental effect on

performance of

various hazards

becomes even more

pronounced. The

compiler can avoid

many hazards

through judicious

selection and

ordering of

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Corporaal ...

WhatIs.com. Very

long instruction word

(VLIW) describes a

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Computer processing architecture in which a language compiler or pre-processor breaks program instruction down into basic operations that can be performed by the processor in parallel (that is, at the same time). These operations are put into a very long instruction word

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which the processor can then take apart without further analysis, handing each operation to an appropriate functional unit.

What is VLIW (very long instruction word)? - Definition ...  
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been teaching

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college students...

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Characteristics ...

VLIW Introduction  
Superscalar Control  
Logic Scaling Each

issued instruction

must be checked

against  $W \cdot L$

instructions, i.e., the  
growth in hardware

$W \cdot (W \cdot L)$  For in-

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order machines,  $L$  is related to pipeline latencies For outof-order machines,  $L$  also includes time spent in instruction buffers (instruction window or ROB)

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